```
1  //*****************************************************************************************
   **********
2  // Header: FUZZY regulator s RTX51 pre taviace pece
3  // File Name: FUZZY.C
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5  // Date: 29.12.2005
6  //*****************************************************************************************
   **********
7
8  #include <reg552.h>
9  #include <stdio.h>
10 #include <stdlib.h>
11 #include <rtx51.h>
12 #include <string.h>
13 #include <dsw.h>
14
15 #define REGULATOR 0
16 #define ACTION    1
17
18 int Error[3];     //int  Error,dError,LastError;
19
20 #define SIZE  5   //Velkost inferencneho bloku !!!
21
22 int X1[SIZE],X2[SIZE],Y[SIZE],Output;
23
24 int Soll,Vert;
25
26 //typedef enum {NM,NS,ZE,PS,PM} State;
27
28 #define NM  0 // Negative medium
29 #define NS  1 // Negative small
30 #define ZE  2 // Zero equal
31 #define PS  3 // Positive small
32 #define PM  4 // Positive medium
33
34 const unsigned char InferencnaTabulka[SIZE][SIZE] =
35 {    /*NM,NS,ZE,PS,PM*///dError  X2[]
36 /*NM*/{PM,PM,PM,PS,ZE},//NM  E
37 /*NS*/{PM,PM,PS,ZE,NS},//NS  r
38 /*ZE*/{PM,PS,ZE,NS,NM},//ZE  r
39 /*PS*/{PS,ZE,NS,NM,NM},//PS  o
40 /*PM*/{ZE,NS,NM,NM,NM} //PM  r
41 };               //    X1[]
42
43 #define K 0x18
44
45 const int VystupnaFunkcia[SIZE] = {-2*K,-K,+0x00,+K,+2*K};
46
47 unsigned int ADC(unsigned char Channel)   //Rutina trva 55us pri 18.432MHz
48 {
49  #define ADCS 0x08
50  #define ADCI 0x10
51  #define ADEX 0x20
52  unsigned int x;
53  ADCON=Channel;                              //Vyber kanalu pre prevod 0..7
54  ADCON|=ADEX+ADCS;                           //Spustim prevod, bolo tu ADCON|=ADEX+ADCS;
55  while((ADCON&ADCI)==0x00);                  //Cakam na ukoncenie prevodu
56  ADCON^=ADCI;                                //Nulujem priznak AD prevodnika
57  x=ADCH*0x04;                                //Normovanie na (int)
58  switch(ADCON&(0x80+0x40))
59   {
60    case 0x40  : x+=0x01; break;
61    case 0x80  : x+=0x02; break;
62    case 0xC0  : x+=0x03; break;
63   }
64  return(x);
65 }
66
67 typedef enum {SKY=0x0400,SK0=+0x0000,SK1=+0x0400,SK2=+0x0800} Osi;
68
69 void Fuzzyfikator(int Value, int *Data) //reentrant
70 {
71  unsigned char i;
72  for(i=0x00; i<SIZE; i++) Data[i]=0x00;
73  if(Value<-SK2) Data[NM]=SKY;
```

```
 74    else if(Value<-SK1)
 75    {
 76     Data[NM]=SKY-(Value+SK2);
 77     Data[NS]=Value+SK2;
 78    }
 79    else if(Value<+SK0)
 80    {
 81     Data[NS]=SKY-(Value+SK1);
 82     Data[ZE]=Value+SK1;
 83    }
 84    else if(Value<+SK1)
 85    {
 86     Data[ZE]=SKY-Value;
 87     Data[PS]=Value;
 88    }
 89    else if(Value<+SK2)
 90    {
 91     Data[PS]=SKY-(Value-SK1);
 92     Data[PM]=Value-SK1;
 93    }
 94    else Data[PM]=SKY;
 95   }
 96
 97   void FuzzyInferencnyModul(int *X1, int *X2, int *Y) //reentrant
 98   {
 99    int min[SIZE];
100    int max,Temp;              //Tu musi byt v pripade RTX51 a LARGE modelu "data int max" s
      optimalizaciou Loop rotation - 6
101    unsigned char i,j,maxpos;
102   // for(i=0x00; i<SIZE; i++) Y[i]=0x00;  //Vymazem vystupny vektor Y[], je to neefektivnejsie
      ako memset()
103    memset(Y,0x00,2*SIZE);         //Vymazem vystupny vektor Y[], je to rychlejsie
104    for(i=0x00; i<SIZE; i++)
105     {
106      for(j=0x00; j<SIZE; j++) if(X1[i]<X2[j]) min[j]=X1[i]; else min[j]=X2[j];
107      max=min[0];
108      maxpos=0x00;
109      for(j=0x01; j<SIZE; j++) if(max<min[j]) {max=min[j]; maxpos=j;}
110      Temp=Y[InferencnaTabulka[i][maxpos]];
111      if(Temp<max) Y[InferencnaTabulka[i][maxpos]]+=max;
112      if(Temp>SKY) Y[InferencnaTabulka[i][maxpos]]=SKY;
113     }
114   }
115
116   int Defuzzyfikator(int *Y) //reentrant
117   {
118    unsigned char i;
119    int ReturnValue=0,SumY=0;   //COG algoritmus - Centrum Of Gravity
120    for(i=0x00; i<SIZE; i++)
121     {
122      SumY+=Y[i];
123      ReturnValue+=Y[i]*VystupnaFunkcia[i];
124     }
125    return((long)ReturnValue<<2)/SumY; //Skalujem - nasobim cislom 4
126   }
127
128   void Regulator(void) _task_ REGULATOR _priority_ 0
129   {
130    os_set_slice(xtal(18.432E6)/10);
131    PWMP=0x20;
132    Soll=Vert=0x0000;
133    while(1)
134     {
135   //   Vert=ADC(0);
136      if(++Vert>=1024) Vert=0;
137      Soll=ADC(2);
138      Error[2]=Error[0];
139      Error[0]=(Vert-Soll)<<1;
140      Error[1]=(Error[0]-Error[2]);
141      Fuzzyfikator(Error[0],X1);
142      Fuzzyfikator(Error[1],X2);
143      FuzzyInferencnyModul(X1,X2,Y);
144      Output=Defuzzyfikator(Y);
145   //   PWM0=0x80-Output;
146      PWM0=PWM1=/*0x80-*/Output;
```

```
147      os_wait(K_TMO,2,NULL);
148     }
149   }
150
151   void main(void)
152   {
153    os_start_system(REGULATOR);
154   }
```